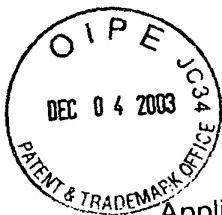


843.37558VX2



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M. FUNABASHI

Application No.: 09/902,672

Filed: July 12, 2001

For: METHOD OF MANUFACTURING A SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Group: 2825

Examiner: L. Malsawma

AMENDMENT

December 4, 2003

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed June 4, 2003, the period of response for which extension is requested by the attached Petition for Extension of Time, please amend the above-identified application as listed below, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.

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